

100

Byte 1	GFC <u>102</u>	VPI <u>104</u>
Byte 2	VPI <u>106</u>	VCI <u>108</u>
Byte 3	VCI <u>110</u>	VCI <u>112</u>
Byte 4	VCI <u>114</u>	PTI <u>116</u> CLP <u>118</u>
Byte 5	HEC <u>120</u>	
Byte 6	<ul style="list-style-type: none">■■■ <p style="text-align: center;">Payload (48 bytes) <u>122</u></p>	
Byte 53		

Conventional ATM cell (53 bytes)

Fig. 1
(prior art)

**"ATM Header Compression Using
Hash Tables"**
Application No.: Unassigned
Applicant: Yan WU, et al.
Attorney Docket No.: 56162.000480
Sheet 2 of 6

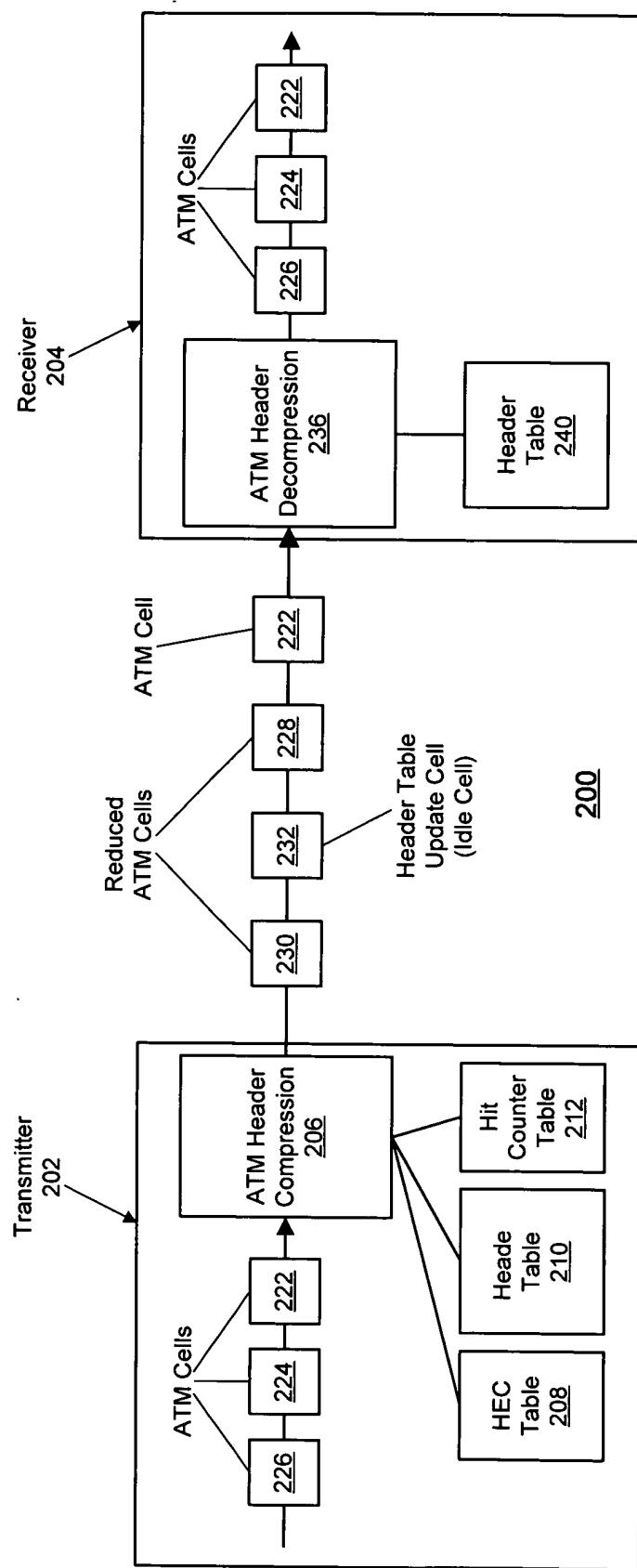


Fig. 2

300

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Byte 1	Flag <u>302</u>	Index <u>304</u>			Resv <u>306</u>	PTI -bit 0 <u>308</u>	Parity <u>310</u>	
Byte 2								
	■				Payload (48 bytes) <u>312</u>			
Byte 49								

Reduced ATM cell (49 bytes)

Fig. 3

400

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Byte 1	Flag <u>402</u>	Idle Cell Index (0x0F) <u>404</u>			Update Flag <u>406</u>	Resv <u>408</u>	Parity <u>410</u>	
Byte 2		CRC <u>412</u>						
Byte 3		Reserved <u>414</u>						
Byte 4		Reserved <u>416</u>						
Byte 5		HEC <u>418</u>						
Byte 6	■	Header Table (4x11) <u>420</u>						
Byte 49	■							

Idle cell (49 bytes)

Fig. 4

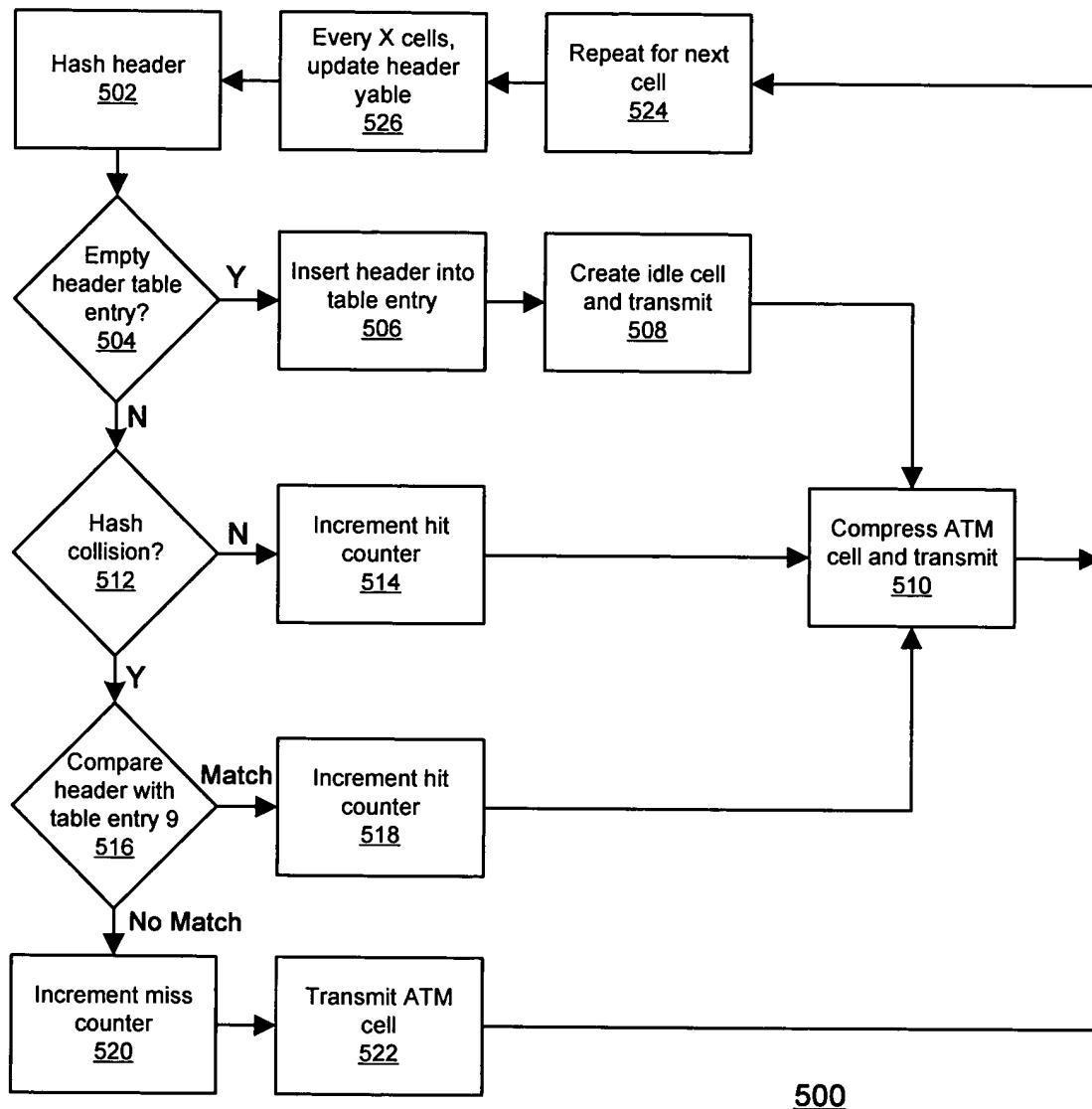


Fig. 5

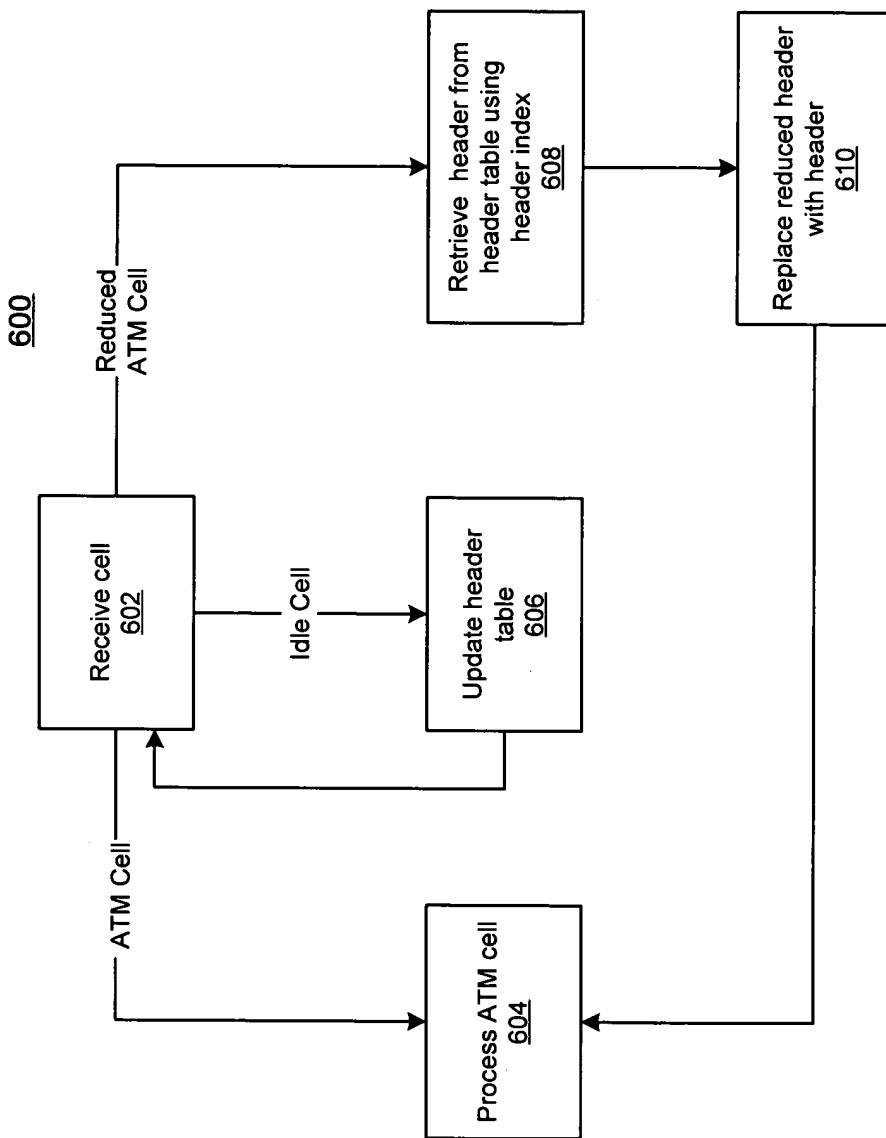


Fig. 6